

Please delete the paragraph heading on page 15 of the subject application, line 1, and insert in place thereof the paragraph heading as follows:

--CLAIMS--

Please insert the paragraph heading on page 15 of the subject application, before claim 1, the following:

-- What is claimed is: --.

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) Device for digital pulse width modulation with:
 - (a) a filter device (11) for filtering a filter input signal (10");
 - (b) a quantization device (13) for quantizing a filter output signal (11') of the filter device (11);
 - (c) a PWM mapper device (15) for generating a digital PWM signal (15') from an output signal (13') of the quantization device (13); and
 - (d) a feedback loop (17) for feeding back the digital PWM signal (15') to a loop input signal (10') and for generating the filter input signal (10") by subtraction.
2. (Currently Amended) Device according to Claim 1, ~~characterized in that~~ wherein an interpolation device (10), in particular an interpolation filter, is provided for generating the loop input signal (10') from an input signal (1).
3. (Currently Amended) Device according to Claim 1 ~~or 2, characterized in that~~ , wherein a post-filter device (16) is provided for filtering the PWM signal (50').
4. (Currently Amended) Device according to ~~one of the preceding claims,~~ Claim 1, wherein a different sampling rate (12) is provided at the filter device (11) than the sampling rate (14) of the quantization device (13).
5. (Currently Amended) Device according to ~~one of the preceding claims,~~ Claim 1, wherein a pulse frequency of the PWM signal (15') corresponds to the sampling frequency (14) of the quantization device (13) and is smaller by a factor of 2^N than the sampling frequency (12) of the filter device (11), N corresponding to the number of bits of the quantization device (13).

6. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein the PWM signal (15') has a constant pulse frequency.
7. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein amplitude values of the output signal (13') of the quantization device (13) can be converted into pulse widths of the PWM signal (15') in the PWM mapper device (15).
8. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein two at least similar feedback loops (17, 17', 11, 13, 15) which are connected to each other on the output side via a load (18) are provided, loop input signals (10', 10') that are inverse in relation to each other being provided on the two loops (17, 17', 11, 13, 15) for generating a differential PWM signal at the load (18).
9. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein a loop filter of the 4th order with a resolution of the quantization device (13) of 4 bits is provided as the filter device (11).
10. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein, for stabilization in the case of overloading, limiting devices (20) are provided in the filter device (11) for limiting output values of integrators (4).
11. (Currently Amended) Device for digital pulse width modulation with:
 - (a) a filter device (19) for filtering a filter input signal (10'') in a first feedback loop (21);
 - (b) a quantization device (13) for quantizing a loop signal (21');
 - (c) a PWM mapper device (15) for generating a digital PWM signal (15') from an output signal (13') of the quantization device (13); and
 - (d) a second feedback loop (22) for feeding back the digital PWM signal (15') to a loop signal (21') while generating the filter input signal (10'')

by subtraction, it being possible for the loop signal (21') to be generated from a loop input signal (40') and a filter output signal (44') by addition.

12. (Currently Amended) Device according to ~~one of the preceding claims,~~ characterized in that Claim 1, wherein an amplifier device and/or filter device is provided downstream of the PWM mapper for amplification and/or filtering of the digital PWM signal and is connected to a voltage supply which is likewise connected to an A/D converter, the output signal of which is connected to a multiplier in the control loop.
13. (Currently Amended) Method for digital pulse width modulation with the steps of:
 - (a) filtering a filter input signal (40'') in a filter device (44);
 - (b) quantizing a filter output signal (44') of the filter device (44) in a quantization device (43);
 - (c) generating a digital PWM signal (45') from the output signal (43') of the quantization device (43) in a PWM mapper device (45); and
 - (d) feeding back the digital PWM signal (45') to a loop input signal (40') and generating the filter input signal (40'') in a feedback loop (47).
14. (Currently Amended) Method according to Claim 13, ~~characterized in that~~ wherein a bandpass pulse width modulation is performed.
15. (Currently Amended) Method according to Claim 13 ~~or 14,~~ characterized in that, wherein an amplifier device and/or filter device is provided downstream of the PWM mapper (45) for amplification and/or filtering of the digital PWM signal (45') and is connected to a voltage supply (25) which is likewise connected to an A/D converter (26), the output signal (27) of which is connected to a multiplier in the control loop (47; 22), the operating voltage signal (25) being digitized in the A/D converter (26) and coupled into the control loop (47, 17', 22).